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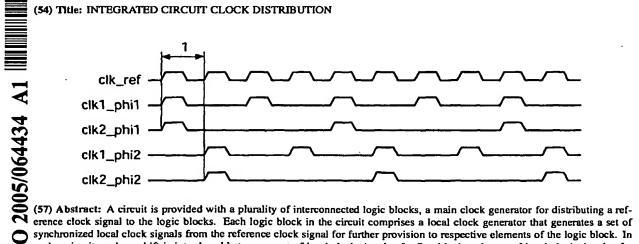
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(54) Title: INTEGRATED CIRCUIT CLOCK DISTRIBUTION



synchronized local clock signals from the reference clock signal for further provision to respective elements of the logic block. In such a circuit, a phase shift is introduced between a set of local clock signals of a first block and a set of local clock signals of a second block.